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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.
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39/192,488 12/30/98 FUJIOKA

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622457 EX-101/1004
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EXAMINER

LOUIS, J

ART UNIT

PAPER NUMBER

DATE MAILED:

10/02/00

10-04-00

Please find below and/or attached an Office communication concerning this application or proceeding.

Commissioner of Patents and Trademarks

SK

Office Action Summary

Application No.
08/182,435

Applicant
Fujloka et al.

Examiner
David L Lewis

Group Art Unit
2778



- ☐ Responsive to communication(s) filed on _____
- ☐ This action is **FINAL**.
- ☐ Since this application is in condition for allowance except for formal matters, **prosecution as to the merits is closed** in accordance with the practice under *Ex parte Quayle*, 35 C.D. 11; 453 O.G. 213.

A shortened statutory period for response to this action is set to expire 3 month(s), or thirty days, whichever is longer, from the mailing date of this communication. Failure to respond within the period for response will cause the application to become abandoned. (35 U.S.C. § 133). Extensions of time may be obtained under the provisions of 37 CFR 1.136(a).

Disposition of Claim

- ☒ Claim(s) 1-12 is/are pending in the application.
- Of the above, claim(s) _____ is/are withdrawn from consideration.
- ☐ Claim(s) _____ is/are allowed.
- ☒ Claim(s) 1-12 is/are rejected.
- ☐ Claim(s) _____ is/are objected to.
- ☐ Claims _____ are subject to restriction or election requirement.

Application Papers

- ☐ See the attached Notice of Draftsperson's Patent Drawing Review, PTO-948.
- ☐ The drawing(s) filed on _____ is/are objected to by the Examiner.
- ☐ The proposed drawing correction, filed on _____ is ☐ approved ☐ disapproved.
- ☐ The specification is objected to by the Examiner.
- ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. § 119

- ☒ Acknowledgement is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d).
- ☒ All ☐ Some* ☐ None of the CERTIFIED copies of the priority documents have been received.
- ☐ received in Application No. (Series Code/Serial Number) _____.
- ☐ received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

*Certified copies not received: _____

- ☐ Acknowledgement is made of a claim for domestic priority under 35 U.S.C. § 119(e).

Attachment(s)

- ☒ Notice of References Cited, PTO-892
- ☐ Information Disclosure Statement(s), PTO-1449, Paper No(s) _____
- ☐ Interview Summary, PTO-413
- ☐ Notice of Draftsperson's Patent Drawing Review, PTO-948
- ☐ Notice of Informal Patent Application, PTO-152

— SEE OFFICE ACTION ON THE FOLLOWING PAGES —

Title: Liquid Crystal Display Device

DETAILED ACTION

Double Patenting

1. Claims 1-12 and in particular 9 are directed to the same invention as that of claim 19 of commonly assigned allowed application, U.S. Serial Number 09/450,436. The issue of priority under 35 U.S.C. 102(g) and possibly 35 U.S.C. 102(f) of this single invention must be resolved.

Since the Patent and Trademark Office normally will not institute an interference between applications or a patent and an application of common ownership (see MPEP § 2302), the assignee is required to state which entity is the prior inventor of the conflicting subject matter. A terminal disclaimer has no effect in this situation since the basis for refusing more than one patent is priority of invention under 35 U.S.C. 102(f) or (g) and not an extension of monopoly.

Failure to comply with this requirement will result in a holding of abandonment of this application.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

Title: Liquid Crystal Display Device

A person shall be entitled to a patent unless --

(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

3. **Claims 1-8 are rejected under 35 U.S.C. 102(a) as being anticipated by Hashimoto (5973660).**
4. **As in claim 1, Hashimoto teaches of a liquid crystal display device comprising: a liquid crystal display panel, column 1 lines 5-7; and for supplying a picture signal line driving circuit for supplying a picture signal voltage to the liquid crystal display panel, column 2 lines 5-42; said picture signal line driving circuit having a switching circuit in which a first transistor, to whose gate electrode a control voltage is applied and a second transistor to whose gate electrode a bias voltage is applied are connected in series, figure 2. As in claims 2, 3, and 4, Hashimoto also teaches of said features, 4 lines 1-50, and figure 2, wherein said features are illustrated in figure 2.**
5. **As in claim 5, Hashimoto teaches of a liquid crystal display device, comprising a liquid crystal display panel and a picture signal line driving circuit for supplying a picture signal voltage to the liquid crystal display panel, column 1 lines 5-7; said picture signal line driving circuit further including: a first output terminal, a second input terminal, and a common output terminal, a first switching element connected between the first input terminal and the common output terminal, and a second switching**

Title: Liquid Crystal Display Device

element connected between the second input terminal and the common output terminal, the first and second switching elements including a transistor at a input port, to whose gate electrode a control voltage is applied connected in series with a transistor at a output port, to whose gate electrode a bias voltage is applied, column 2 lines 5-47, figure 2, 3, and 15 . As in **claims 6 and 7**, Hashimoto teaches of said features, figure 3, column 4 lines 1-60, wherein said features are illustrated in figures 2 and 3.

6. **Claims 9-12 are rejected under 35 U.S.C. 102(a) as being anticipated by Kitamura (5682175).**
7. **As in claim 9, Kitamura teaches of a liquid crystal display device, comprising a liquid crystal display panel and a picture signal line driving circuit for supplying a picture signal voltage to the liquid crystal display panel, column 1 lines 5-13; the picture signal line driving circuit further comprising: a first output circuit for outputting a positive polarity picture signal voltage, a second output circuit for outputting a negative polarity picture signal voltage, and a switching circuit for switching the positive polarity picture signal voltage supplied from the first output circuit and the negative polarity picture signal voltage supplied from the second output circuit to a pair of picture signal lines and outputting the voltages, column 2 lines 20-43, the switching circuit further including: a first switching element connected between the first output circuit and the first picture signal line of the picture signal line pair, a third switching element connected between the first output circuit and the second picture signal**

Title: Liquid Crystal Display Device

line of the picture signal line pair, a second switching element connected between the second output circuit and the second picture signal line, and a fourth switching element connected between the second output circuit and the first picture signal line, wherein a positive-polarity picture signal voltage supplied from the first output circuit is output to the first or second picture signal line by selectively turning on/off the first, second, third, and fourth switching elements, a negative-polarity picture signal voltage supplied from the second output circuit is output to the second or first picture signal line by selectively turning on/off the first, second, third, and fourth switching elements, and the switching elements are constituted by connecting a transistor at an output circuit to whose gate electrode a control voltage is applied in series with a transistor at a picture signal line side to whose gate electrode a constant bias voltage is applied, column 4 lines 24-76, column 5 lines 1-11, and figure 5. As in **claims 10, 11, and 12**, Kitamura also teaches of said features, column 2 lines 20-43, figures 4 and 5.

Conclusion

8. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. 6069605, 6025835, 5886679, 6049321..

Title: Liquid Crystal Display Device

9. Any inquiry concerning this communication or earlier communications from the examiner should be directed to **David L. Lewis** whose telephone number is **(703) 306-3026**. The examiner can normally be reached on MT and THF from 8 to 5. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bipin Shalwala, can be reached on (703) 305-4938. Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (703) 305-3900.

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

Washington, D.C. 20231

Or faxed to:


(703) 308-9051, (for formal communications intended for entry)

Or:

(703) 308-6606 (for informal or draft communications, please label "PROPOSED" or "DRAFT")

Or hand-delivered to:

Crystal Park II, 2121 Crystal Drive, Arlington, VA., Sixth Floor (Receptionist).


BIPIN SHALWALA
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2700